

**A Phase-Interpolation Circuit and A Phase-Interpolation Signal Generating
Device Applying the Same**

Abstract of the Disclosure

The invention relates to a phase-interpolation circuit and a
5 phase-interpolation signal generating circuit applying the phase-interpolation
circuit. The phase-interpolation circuit can avoid short-circuit current effectively. In
addition, an inter-phase signal can be interpolated between the rising edge and
the falling edge of the clock pulse. The phase-interpolation signal generating
device can generate multiphase clock signals which not only have linearly
10 distributed phases but also maintain good 50% duty cycle of the multiphase clock
signals.